

**THREE-DIMENSIONAL INTEGRATED CIRCUIT DESIGN
(SYSTEMS ON SILICON)**

Eric Stokke

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Chapter 2 - Manufacturing of Three-Dimensional Packaged Systems. Pages 35 Design issues for the on-chip inductors are discussed. Models of crosstalk.

Three-Dimensional Integrated Circuit Design, Second Edition, expands the original emphasizing not only physical design, but also algorithms and system-level silicon vias; Substrate noise coupling in heterogeneous 3-D ICs; Design of 3-D.

3. Energy-efficient circuit design. Emerging integrated circuit technologies .. in 3D Systems-on-Chip," Physical Design for 3D Integrated Circuits, A. Todri-

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